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LIGHT ELECTRONICS CO., LTD.





Photo Transistor Diode Specification

Commodity: Photo Transistor

●Ic(on) Bin Code (V_{CE}=5V, Ee=1mW/cm², p=940nm)

BIN CODE	Min. (mA)	Max. (mA)
X2	3.7	4.4
X3	4.4	5.3

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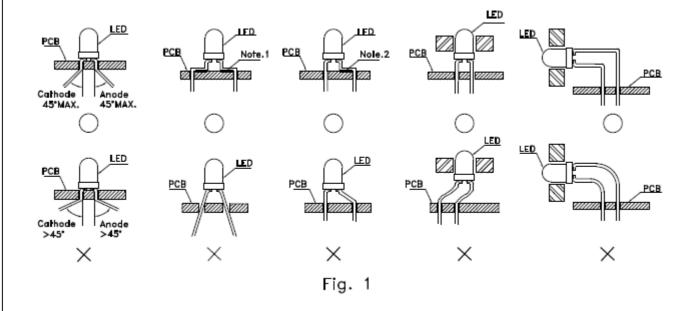
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LED MOUNTING METHOD

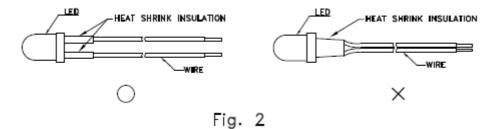
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1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures (Fig.1).

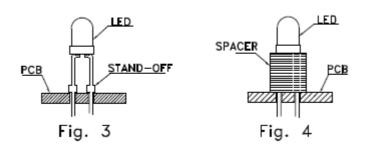


Note 1-2: Do not route PCB trace in the contact area between the lead frame and the PCB to prevent short-circuits.

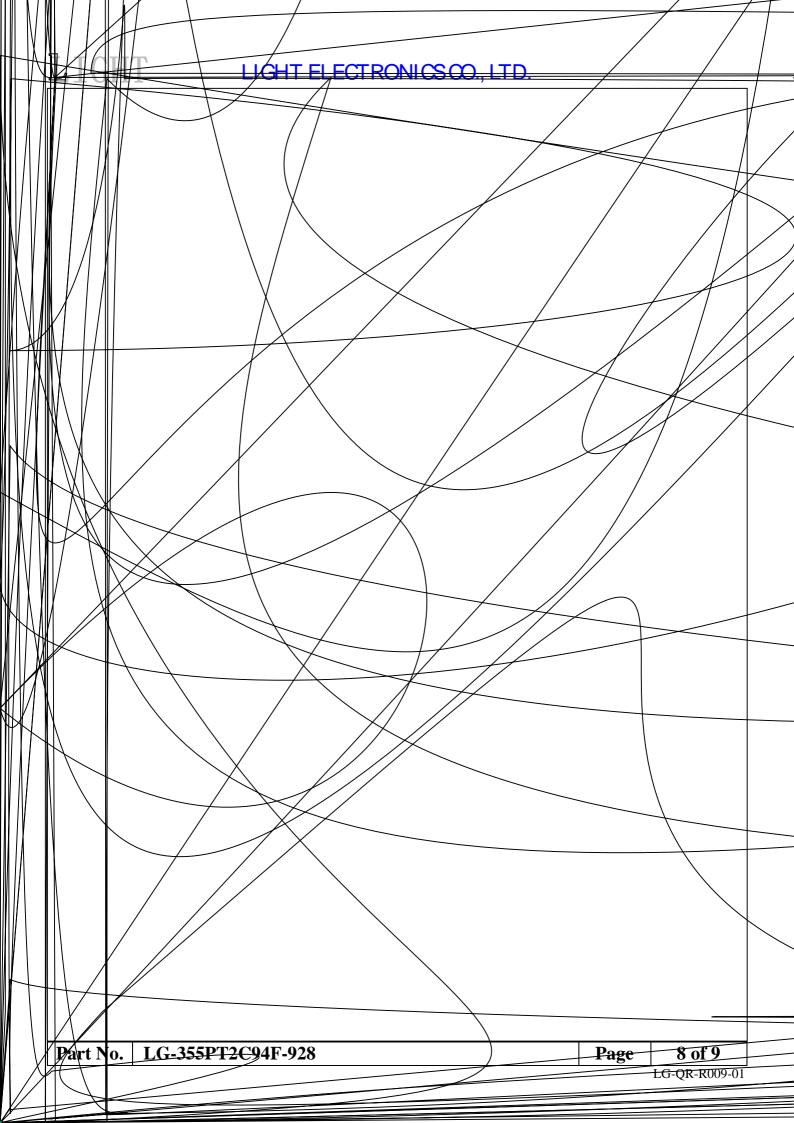
2. When soldering wire to the LED, use individual heat-shrink tubing to insulate the exposed leads to prevent accidental contact short-circuit (Fig.2).



3. Use stand-offs (Fig.3) or spacers (Fig.4) to securely position the LED above the PCB.



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PACKAGE LIGHT Bag V Bag minimum volume **Bag volume** Inner box volume **Outer carton volume** (pcs / Bag) (pcs / Bag) (Bag / box) (Box / Carton) 500 1000 10 4 LG-355PT2C94F-928 Part No. Page 9 of 9